

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sau Ching Wong
Assignee: Multi Level Memory Technology
Title: Efficient Redundancy System for Flash Memories with Uniformly Sized Blocks (As amended)
Serial No.: Unknown Filing Date: June 30, 2003
Examiner: Hur, Jung H. Group Art Unit: 2824
Docket No.: MLM003US1D

San Jose, California
June 30, 2003

Mail Stop Patent Application
COMMISSIONER FOR PATENTS
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PRELIMINARY AMENDMENT

Dear Sir:

Please amend the accompanying patent application as follows.